

SPECIFICATIONS

OUTLINE DRAWING

Modulation Input

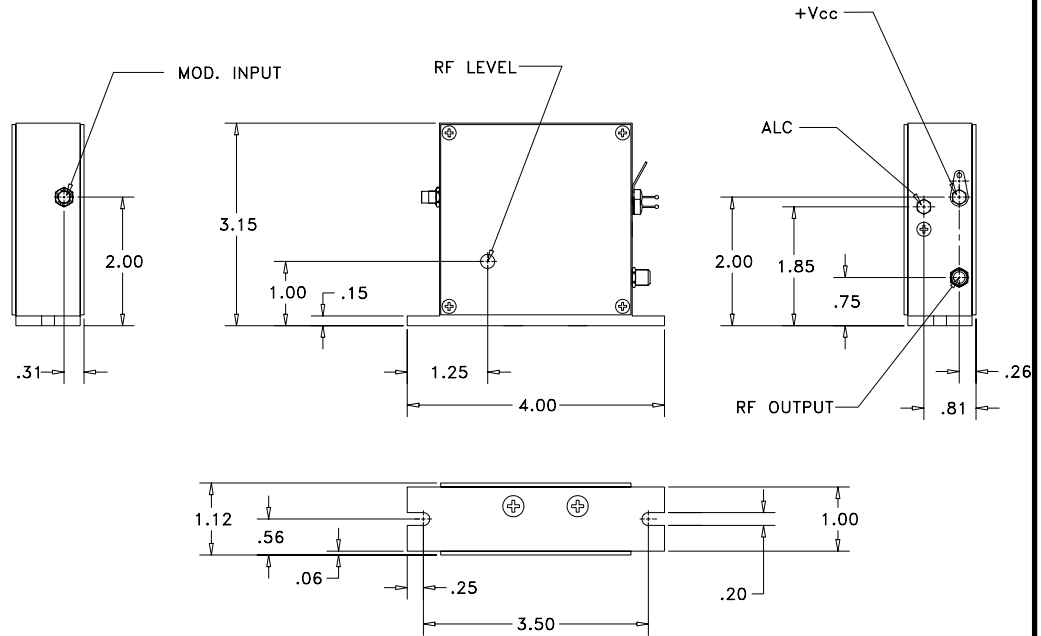
Input Impedance
Analog Input (SMB Male)

50 Ohms
0 to +1.0 VDC

RF Output

Center Frequency (Fc)
Output Power (SMA Female)
Rise/Fall Time
RF Contrast Ratio
Harmonic Distortion
Output Impedance
Output VSWR
Modulation Bandwidth
Power Supply Voltage (+Vcc)

125 MHz \pm 0.1%
2.0 W
12 nsec Typ.
35 dB min
-30 dBc
50 Ohms
1.5 : 1 Max
60 MHz
+28 V @ 600 mA



Notes:

1. The slope of the RF output power vs. the input signal voltage curve shall be non-zero and positive at all points between 0 and 1.0 Volts input, inclusive.
2. Output power factory set to 2 W at 1 Volt input. Power stability less than 5% over the heat sink's ambient temperature range of 0-40° C, after 5 minute warm-up.
3. When calculating the contrast ratio, it is understood that only the power of the 125 MHz fundamental shall be used. The higher harmonics have no effect on the AO modulator's performance.

For Reference Only

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TOLERANCES: .XX \pm .01 .XXX \pm .005	DR	A. Campi 12/3/1997	Crystal Technology, Inc. DESCRIPTION: AODR 1125AF-AIF0-2.0
MATERIAL:	CHK		
FINISH:	APP		
	APP		PART NUMBER: 97-02207-15
			REV: 0
			1 of 1