

SPECIFICATIONS

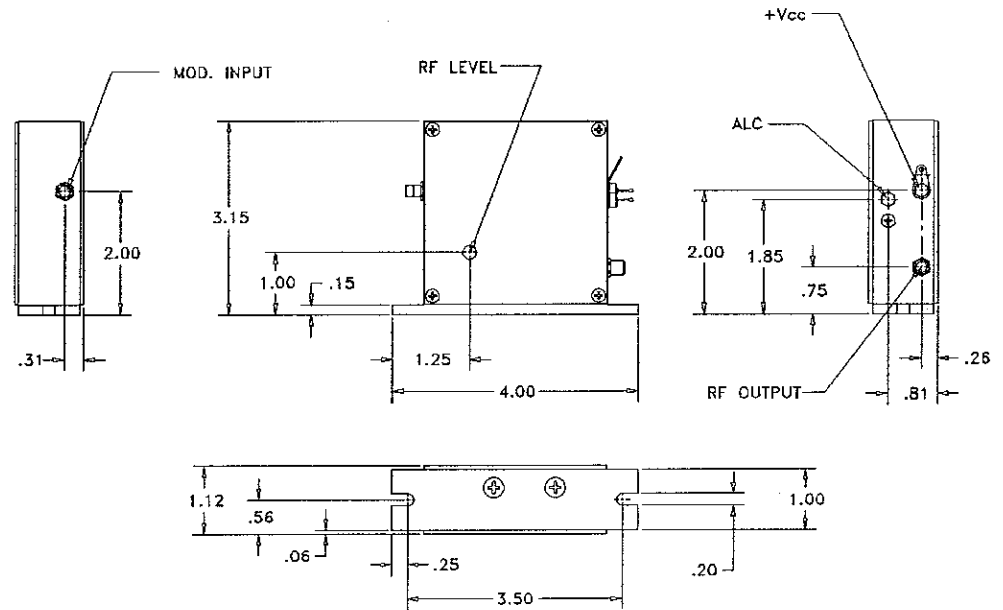
Modulation Input

Input Impedance 50 Ohms
 Digital Input (SMB Male) Standard TTL Levels

RF Output

Center Frequency (Fc) 200 MHz \pm 0.1%
 Output Power (SMA Female) 2.0 W
 Rise/Fall Time 5 nsec Max
 RF Contrast Ratio 35 dB min
 Harmonic Distortion -30 dBc
 Output Impedance 50 Ohms
 Output VSWR 1.5 : 1 Max
 Power Supply Voltage (Filtered Feedthru) +28 V @ 600 mA
 ALC Voltage Level (Filtered Feedthru) +0 to +25 V nominal
 ALC Bandwidth 35 kHz
 RF Level Potentiometer Range 1.0 - 2.0 W

OUTLINE DRAWING



Notes:

- Output power factory set to 2 W at a 2.4 V, 30 mA input. Power stability less than 5% over the heat sink's ambient temperature range of 0-40° C, after 5 minute warm-up.
- When calculating the contrast ratio, it is understood that only the power of the 200 MHz fundamental shall be used. The higher harmonics have no effect on the AO modulator's performance.
- A +25 Volt nominal input on the ALC corresponds to full RF output power. Zero RF power occurs at an ALC voltage slightly above 0 Volts. Full RF power occurs if ALC input is left unconnected.

**For Reference
Only**

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TOLERANCES: .XX \pm .01 .XXX \pm .005	DR	T. Pham 12/3/97	Crystal Technology, Inc.		
MATERIAL:	CHK	RAC 1/8/98	DESCRIPTION: AODR 1200AF-DIF0-2.0		
FINISH:	APP	2/7/98	PART NUMBER:	REV:	SHEET 1 OF 1
	APP		97-02207-16	C	