

**SPECIFICATIONS**

**OUTLINE DRAWING**

Modulation Input

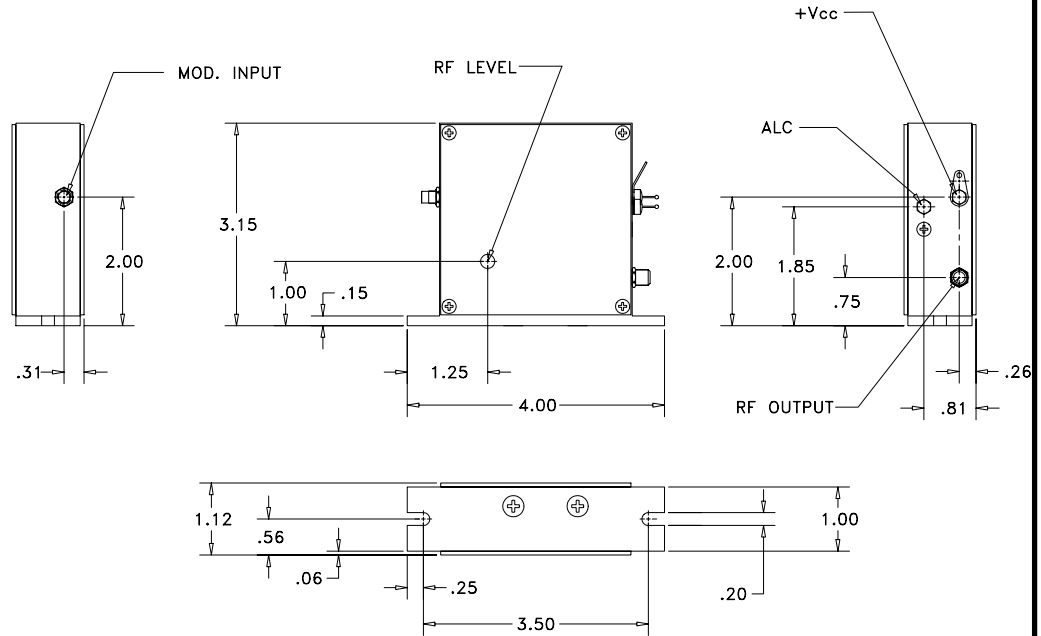
Input Impedance  
Digital Input (SMB Male)

50 Ohms  
Standard TTL Levels

RF Output

Center Frequency (Fc)  
Output Power (SMA Female)  
Rise/Fall Time  
RF Contrast Ratio  
Harmonic Distortion  
Output Impedance  
Output VSWR  
Bandwidth  
Power Supply Voltage (+Vcc)

80 MHz  $\pm$  0.1%  
1.0 W  
12 nsec Typ.  
35 dB min  
-30 dBc  
50 Ohms  
1.5 : 1 Max  
60 MHz  
+24 V @ 600 mA



Notes:

1. Output power factory set to 1.0 W at a 2.4 V, 30 mA input. Power stability less than 5% over the heat sink's ambient temperature range of 0-40° C, after 5 minute warm-up.
2. When calculating the contrast ratio, it is understood that only the power of the 80 MHz fundamental shall be used. The higher harmonics have no effect on the AO modulator's performance.

**For Reference  
Only**

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TOLERANCES: .XX $\pm$ .01 .XXX $\pm$ .005	DR	G. Scholz 8/24/2004	<b>Crystal Technology, Inc.</b> DESCRIPTION: <b>AODR 1080AF-DIF0-1.0</b>
MATERIAL:	CHK		
FINISH:	APP		
	APP		PART NUMBER: 97-02207-53
			REV: 0
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