

**SPECIFICATIONS**

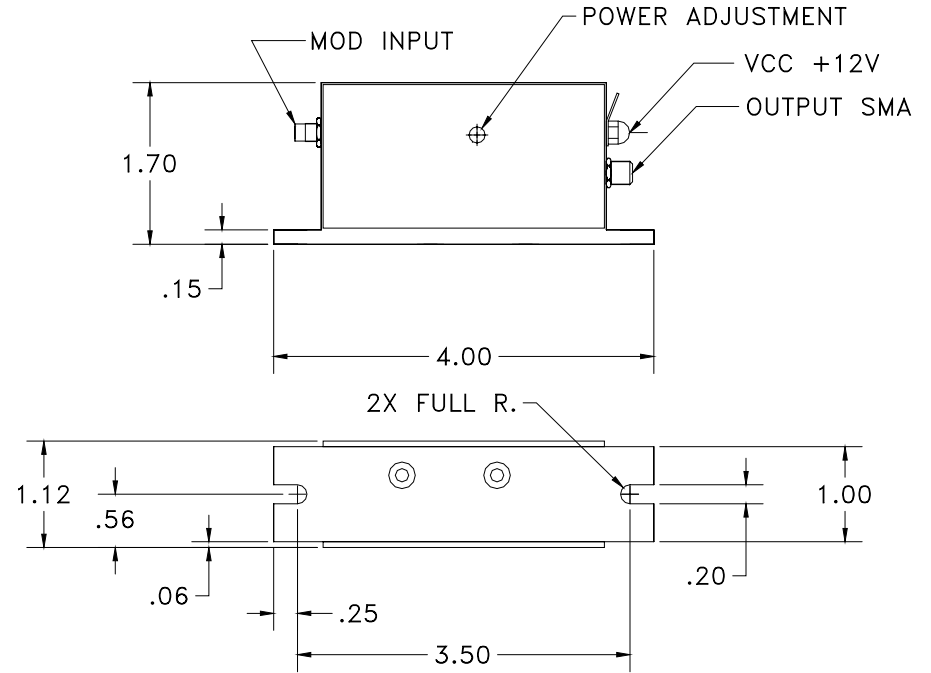
Modulation Input

Input Impedance 50 Ohms  
 Analog Input (SMB) 0 to + 1.0 VDC

RF Output

Center Frequency (Fc) 80 MHz  $\pm$  0.1%  
 Output Power 0.5 W  
 Rise/Fall Time 10 nsec Typ.  
 RF Contrast Ratio 35 dB min  
 Harmonic Distortion NA  
 Output Impedance 50 Ohms  
 Output VSWR 1.5 : 1 Max  
 Bandwidth 40 MHz  
 Output Connector SMA  
 Power Supply Voltage (Vcc) +12V @ 300 mA

**OUTLINE DRAWING**



Notes:

1. The slope of the RF output power vs. the input signal voltage curve shall be non-zero and positive at all points between 0 and 1.0 Volts input, inclusive.
2. Output power factory set to 0.5 W at 1 Volt input. Power stability less than 5% over the heat sink's ambient temperature range of 0-40° C, after 5 minute warm-up.
3. When calculating the contrast ratio, it is understood that only the power of the 80 MHz fundamental shall be used. The higher harmonics have no effect on the AO modulator's performance.

**For Reference Only**

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TOLERANCES: .XX $\pm$ .01 .XXX $\pm$ .005	DR	G. Scholz 4/19/2002	<b>Crystal Technology, Inc.</b> DESCRIPTION: <b>AODR 1080LP-AINO-0.5</b>
MATERIAL:	CHK		
FINISH:	APP		
	APP		PART NUMBER: 97-02698-02
			REV: 0
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